

caeleste

PCB design engineer



About Caeleste

Caeleste is an innovative group of engineers, scientists & enthusiasts who innovate CMOS image sensors. We push the boundaries to create unique, “beyond state-of-the art” solutions that differentiate our customers from their competitors. We are a global company based in Belgium, partnering with world-class, multi-national companies in the space, scientific, medical, industrial, and life science industries. At Caeleste, having fun goes hand-in-hand with our innovative mindset.

By becoming a part of Caeleste, you will be able to experience the direct impact of your ideas and actions, regardless of your role or seniority.

PCB design engineer

To strengthen our system and test team we are looking for an engineer to design PCBs and CoBs used to validate & characterize novel CMOS image sensors. Your responsibilities cover a broad scope ranging from PCB architecture, schematic design & layout, functional & signal integrity simulation, hands-on bring-up of new hardware platforms and an occasional 3D printed design. In close collaboration with FPGA and software engineers you assist in debug & characterization of new CMOS image sensor prototypes (and production runs) on your hardware platforms. With increasing complexity of the image sensors, your designs need to bring high-speed analog & digital prototypes & products alive to accommodate complex sensors & data acquisition systems. You will work in close collaboration with chip design engineers, project managers and you will interact with and manage external suppliers and service providers.

Although the prime focus is a PCB design environment and supplier interaction, your thorough upfront analysis, documented and first-time-right approach is critical for a quality focused collaboration in a team of experts that depend on your crucial contributions in a project driven organization. You will have a significant amount of autonomy and will take up impactful responsibilities in the team. We foster experience, but also welcome junior engineers with great potential.

Job specific requirements:

- Master of science in (Micro) Electronics, Physics or equivalent by experience.
- Experienced in analog/digital circuit design & simulation software (Altium designer or Orcad)
- Experienced in board bring-up and debug
- Understanding of mixed digital/analog electronics incl. power supplies and high-speed design
- Hands-on experience with electronic test & measurement equipment
- Thorough understanding of PCB & packaging technology and materials
- Capable of working with Python scripting
- Creativity, being enhancing and result driven are valued
- Good communication skills in English in writing and presenting
- Accountable & committed team player

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Additional skills that are of interest:

- Background in test & measurement of analog & digital semiconductors
- Background of high-speed interfaces (LVDS, FPGA, transceivers), ADC technology & memory standards is a plus
- Background of FPGA board level requirements (preferably Xilinx) is a plus
- Background in Silicon processing and CMOS technology
- Background in Optics and Solid-state Physics
- Experience in image sensors, their applications, camera or instrument design is a plus
- Experience in electro-optical characterization or evaluation of image sensors is a plus
- Experience with materials at cryogenic temperatures
- Experience with joining & sealing processes and hermeticity
- Experience with 3D CAD, mechanical drawings with Autodesk Inventor
- Working language is English, any other language is a plus

We offer:

- A competitive compensation & benefits package
- A competitive & international context of world-leading companies
- A technological playground within a mixed-skilled, multi-cultural team of experts
- A continuous focus on learning at Caeleste University
- A flexible & pragmatic environment with attention to teamwork and work-life balance

For all your questions, please contact Ewa Burzynska or jobs@caeleste.be